

Notice of References Cited	Application/Control No. 10/022,972	Applicant(s)/Patent Under Reexamination KUZEMCHAK ET AL.	
	Examiner Chih-Ching Chow	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,658,578	12-2003	Laurenti et al.	713/324
	B	US-5,949,993	09-1999	Fritz, David Carroll	703/22
	C	US-6,308,263	10-2001	Hayakawa et al.	712/230
	D	US-5,724,563	03-1998	Hasegawa, Katsuya	712/233
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	S					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Cycle and Phase Accurate DSP Modeling and Integration for HW/SW Co-Verification", 1999 ACM, by Lisa Guerra, et al.
	V	"A Scalable Formal Verification Methodology for Pipelined Micorprocessors", 1996 ACM, by Jeremy Levitt and Kunle Olukotun.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.